

AMENDMENTS

In the Claims

The following is a marked-up version of the claims with the language that is underlined (“ ”) being added and the language that contains strikethrough (“~~—~~”) being deleted:

1. (Currently Amended) A method for allocating a shared resource among a plurality of devices, the method comprising the steps of:

associating a bucket to each one of the plurality of devices wherein the plurality of devices share a shared resource;

assigning a fill rate to each bucket where each bucket accrues a predetermined number of credits for each time period the associated device is stalled, wherein each fill rate is different, each fill rate indicating access priority assigned to the associated device;

assigning a drain rate to each bucket where each bucket drains a predetermined number of credits for each time period the associated device is granted access to the shared resource, wherein each drain rate is different, each drain rate indicating access priority assigned to the associated device;

comparing each bucket to determine a grant bucket having the most number of credits at a specific time; and

granting access to the shared resource to the device associated with the grant bucket.

2. (Original) The method of claim 1, wherein the shared resource comprises a memory bandwidth.

3. (Original) The method of claim 2, wherein memory bandwidth is associated with one or more of DRAM, SDRAM, SRAM and EPROM.

4. (Original) The method of claim 1, wherein the shared resource comprises a bus connected to at least one peripheral device including one or more of TDM, UART, USB, and PCI.

5. (Original) The method of claim 1, wherein the plurality of devices comprise processing units.

6. (Original) The method of claim 1, wherein the plurality of devices comprise at least a combination of a DMA controller, a network processor and a protocol processor.

7. (Cancelled)

8. (Cancelled)

9. (Original) The method of claim 1, further comprising the step of:
determining a maximum latency when a bucket reaches a maximum number of credits.

10. (Original) The method of claim 9, further comprising the step of:
granting immediate access to the shared resource to the device associated with bucket reaching the maximum number of credits when the maximum latency is determined.

11. (Original) The method of claim 1, further comprising the step of:
dynamically adjusting one or more of the fill rate and drain rate associated with one or more buckets for load balancing one or more of the plurality of devices.

12. (Original) The method of claim 1, further comprising the step of:
determining an amount of bandwidth each device has used.

13. (Original) The method of claim 12, further comprising the step of:
charging an entity according to the amount of bandwidth used.

14. (Currently Amended) A system for allocating a shared resource among a plurality of devices, the system comprising:

an association module for associating a bucket to each one of the plurality of devices wherein the plurality of devices share a shared resource;

a fill rate module for assigning a fill rate to each bucket where each bucket accrues a predetermined number of credits for each time period the associated device is stalled, wherein each fill rate is different, each fill rate indicating access priority assigned to the associated device;

a drain rate module for assigning a drain rate to each bucket where each bucket drains a predetermined number of credits for each time period the associated device is granted access to the shared resource, wherein each drain rate is different, each drain rate indicating access priority assigned to the associated device.;

a grant determination module for comparing each bucket to determine a grant bucket having the most number of credits at a specific time; and

a grant access module for granting access to the shared resource to the device associated with the grant bucket.

15. (Original) The system of claim 14, wherein the shared resource comprises a memory bandwidth.

16. (Original) The system of claim 15, wherein memory bandwidth is associated with one or more of DRAM, SDRAM, SRAM and EPROM.

17. (Original) The system of claim 14, wherein the shared resource comprises a bus connected to at least one peripheral device including one or more of TDM, UART, USB, and PCI.

18. (Original) The system of claim 14, wherein the plurality of devices comprise processing units.

19. (Original) The system of claim 14, wherein the plurality of devices comprise at least a combination of a DMA controller, a network processor and a protocol processor.

20. (Cancelled)

21. (Cancelled)

22. (Original) The system of claim 14, wherein a maximum latency is determined when a bucket reaches a maximum number of credits.

23. (Original) The system of claim 22, wherein immediate access to the shared resource is granted to the device associated with bucket reaching the maximum number of credits when the maximum latency is determined.

24. (Original) The system of claim 14, wherein one or more of the fill rate and drain rate associated with one or more buckets is dynamically adjusted for load balancing one or more of the plurality of devices.

25. (Original) The system of claim 14, wherein an amount of bandwidth each device has used is determined.

26. (Original) The system of claim 25, wherein an entity is charged according to the amount of bandwidth used.